



New Product

Si4814BDY  
Vishay Siliconix

## Dual N-Channel 30-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY				
	V <sub>DS</sub> (V)	r <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ)
Channel-1	30	0.018 @ V <sub>GS</sub> = 10 V	10	6.6
		0.023 @ V <sub>GS</sub> = 4.5 V	8.5	
Channel-2		0.018 @ V <sub>GS</sub> = 10 V	10.5	8.9
		0.022 @ V <sub>GS</sub> = 4.5 V	9.3	

SCHOTTKY PRODUCT SUMMARY		
V <sub>DS</sub> (V)	V <sub>SD</sub> (V)—Diode Forward Voltage	I <sub>F</sub> (A)
30	0.50 V @ 1.0 A	2.0

### FEATURES

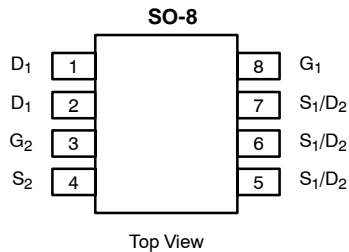
- LITTLE FOOT® Plus Integrated Schottky
- 100% R<sub>g</sub> Tested

### APPLICATIONS

- DC/DC Converters
  - Notebook

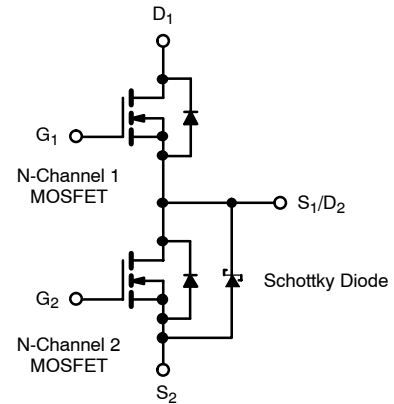


Product Is Completely Pb-free



Top View

Ordering Information: Si4814BDY—E3  
Si4814BDY-T1—E3 (with Tape and Reel)



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Channel-1	Channel-2	Unit
Drain-Source Voltage		V <sub>DS</sub>	30		V
Gate-Source Voltage		V <sub>GS</sub>	20		
Continuous Drain Current (T <sub>J</sub> = 150°C) <sup>a, b</sup>	T <sub>C</sub> = 25°C	I <sub>D</sub>	10	10.5	A
	T <sub>C</sub> = 70°C		8	8.3	
	T <sub>A</sub> = 25°C		7.5 <sup>a, b, c</sup>	7.8 <sup>a, b, c</sup>	
	T <sub>A</sub> = 70°C		6 <sup>a, b, c</sup>	6.3 <sup>a, b, c</sup>	
Pulsed Drain Current (10 μs Pulse Width)		I <sub>DM</sub>	40	40	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25°C	I <sub>S</sub>	3	3.2	
	T <sub>A</sub> = 25°C		1.7 <sup>a, b, c</sup>	1.8 <sup>a, b, c</sup>	
Pulsed Source-Drain Current		I <sub>SM</sub>	40	40	
Single-Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	15		
Single-Pulse Avalanche Energy		E <sub>AS</sub>	11.2		mJ
Maximum Power Dissipation <sup>a, b</sup>	T <sub>C</sub> = 25°C	P <sub>D</sub>	3.3	3.5	W
	T <sub>C</sub> = 70°C		2.1	2.2	
	T <sub>A</sub> = 25°C		1.9 <sup>a, b, c</sup>	2.0 <sup>a, b, c</sup>	
	T <sub>A</sub> = 70°C		1.2 <sup>a, b, c</sup>	1.3 <sup>a, b, c</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150		°C

Notes

- Based on T<sub>C</sub> = 25°C.
- Surface Mounted on 1" x 1" FR4 Board.
- t = 10 sec



THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Channel-1		Channel-2		Unit
			Typ	Max	Typ	Max	
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ sec	$R_{thJA}$	54	65	47	60	°C/W
Maximum Junction-to-Foot (Drain)	Steady-State	$R_{thJF}$	32	38	30	35	

## Notes

- a. Surface Mounted on 1" x 1" FR4 Board.  
b. Maximum under steady state conditions is 112 °C/W for Channel 1 and 107 °C/W for Channel 2.

MOSFET SPECIFICATIONS ( $T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)							
Parameter	Symbol	Test Condition	Min	Typ <sup>a</sup>	Max	Unit	
<b>Static</b>							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	Ch-1 30 Ch-2 30			V	
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\ \mu\text{A}$	Ch-1 Ch-2	24 25		mV/°C	
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$		Ch-1 Ch-2	-6 -6			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	Ch-1 Ch-2	1.5 1.5	3.0 2.7		
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = 20\text{ V}$	Ch-1 Ch-2		100 100	nA	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$	Ch-1 Ch-2		1 100	$\mu\text{A}$	
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 85^\circ\text{C}$	Ch-1 Ch-2		15 2000		
On-State Drain Current <sup>b</sup>	$I_{D(on)}$	$V_{DS} = 5\text{ V}, V_{GS} = 10\text{ V}$	Ch-1 Ch-2	20 20		A	
Drain-Source On-State Resistance <sup>b</sup>	$r_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	Ch-1		0.0145	0.018	$\Omega$
		$V_{GS} = 10\text{ V}, I_D = 10.5\text{ A}$	Ch-2		0.015	0.018	
		$V_{GS} = 4.5\text{ V}, I_D = 8.5\text{ A}$	Ch-1		0.019	0.023	
		$V_{GS} = 4.5\text{ V}, I_D = 9.3\text{ A}$	Ch-2		0.018	0.022	
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = 15\text{ V}, I_D = 10\text{ A}$	Ch-1		30	S	
		$V_{DS} = 15\text{ V}, I_D = 10.5\text{ A}$	Ch-2		35		
Diode Forward Voltage <sup>b</sup>	$V_{SD}$	$I_S = 1.7\text{ A}, V_{GS} = 0\text{ V}$	Ch-1		0.75	1.1	V
		$I_S = 1\text{ A}, V_{GS} = 0\text{ V}$	Ch-2		0.47	0.5	
<b>Dynamic<sup>a</sup></b>							
Total Gate Charge	$Q_g$	Channel-1 $V_{DS} = 15\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 10\text{ A}$ Channel-2 $V_{DS} = 15\text{ V}, V_{GS} = 4.5\text{ V}, I_D = -10.5\text{ A}$	Ch-1 Ch-2		6.6 8.9	10 14	nC
Gate-Source Charge	$Q_{gs}$		Ch-1 Ch-2		2.9 3.4		
Gate-Drain Charge	$Q_{gd}$		Ch-1 Ch-2		2.3 2.4		
Gate Resistance	$R_g$		Ch-1 Ch-2	0.5 0.5	1.9 2.3	2.9 3.5	



MOSFET SPECIFICATIONS (T <sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)								
Parameter	Symbol	Test Condition	Min	Typ <sup>a</sup>	Max	Unit		
<b>Dynamic<sup>a</sup></b>								
Turn-On Delay Time	t <sub>d(on)</sub>	Channel-1 V <sub>DD</sub> = 15 V, R <sub>L</sub> = 15 Ω I <sub>D</sub> ≅ 1 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 6 Ω  Channel-2 V <sub>DD</sub> = 15 V, R <sub>L</sub> = 15 Ω I <sub>D</sub> ≅ 1 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 6 Ω	Ch-1		8	15	ns	
			Ch-2		9	15		
Rise Time	t <sub>r</sub>		Ch-1		11	18		
			Ch-2		13	20		
Turn-Off Delay Time	t <sub>d(off)</sub>		Ch-1		21	32		
			Ch-2		27	40		
Fall Time	t <sub>f</sub>		Ch-1		6	10		
			Ch-2		9	15		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>		I <sub>F</sub> = 1.3 A, di/dt = 100 A/μs	Ch-1		28		40
			I <sub>F</sub> = 2.2 A, di/dt = 100 μA/μs	Ch-2		24		35
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 1.3 A, di/dt = 100 A/μs	Ch-1		17		nC	
		I <sub>F</sub> = 2.2 A, di/dt = 100 μA/μs	Ch-2		12			
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = 1.3 A, di/dt = 100 A/μs	Ch-1		12		ns	
		I <sub>F</sub> = 2.2 A, di/dt = 100 μA/μs	Ch-2		11			
Reverse Recovery Rise Time	t <sub>b</sub>	I <sub>F</sub> = 1.3 A, di/dt = 100 A/μs	Ch-1		16			
		I <sub>F</sub> = 2.2 A, di/dt = 100 μA/μs	Ch-2		13			

Notes

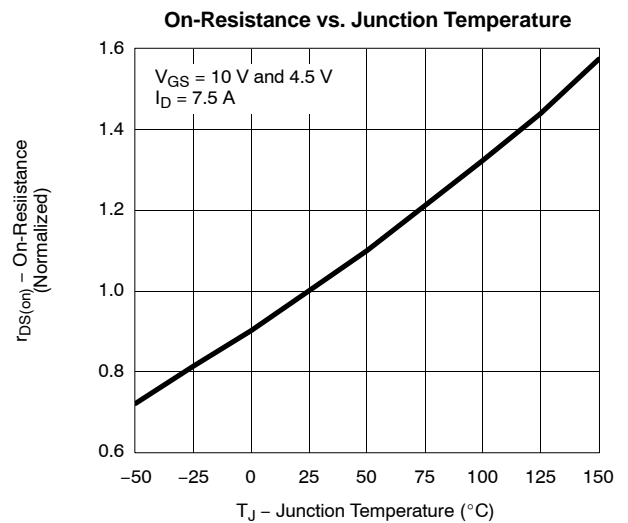
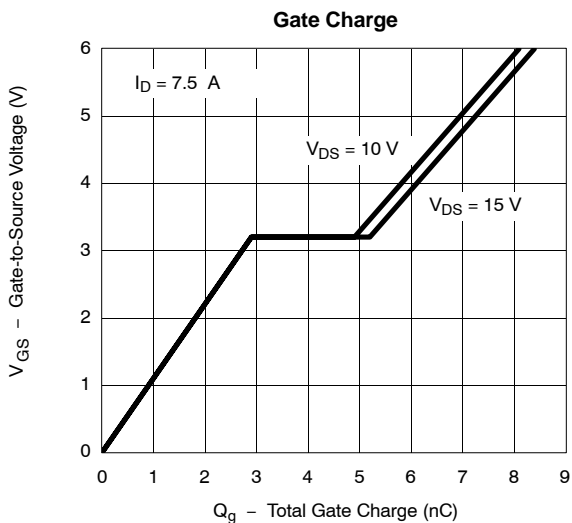
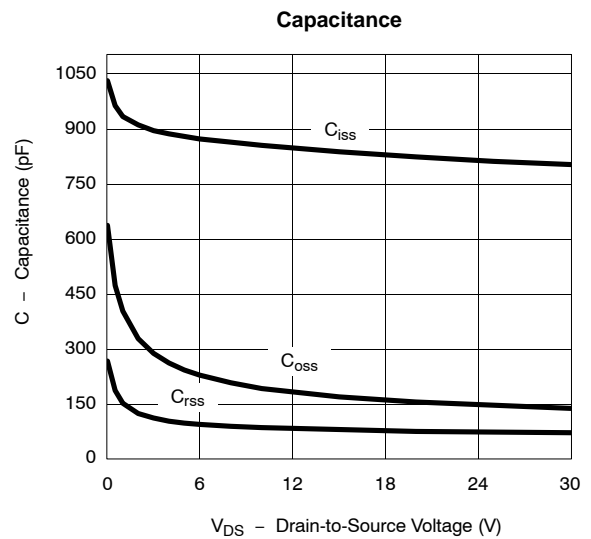
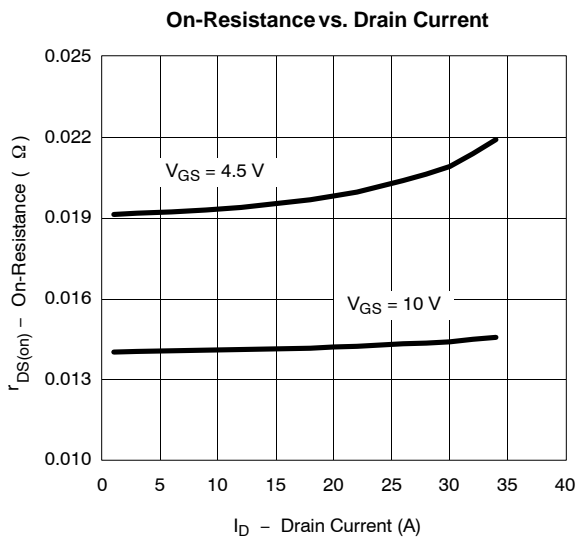
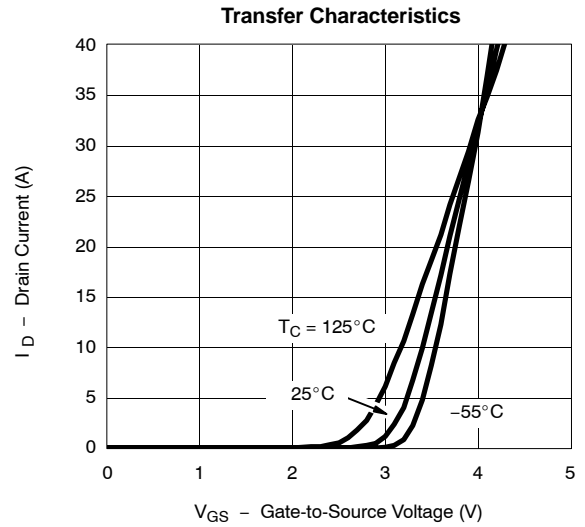
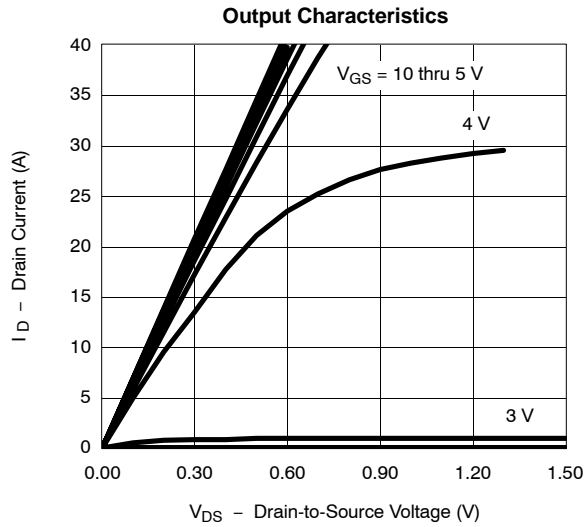
- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.

SCHOTTKY SPECIFICATIONS (T <sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Forward Voltage Drop	V <sub>F</sub>	I <sub>F</sub> = 1.0 A		0.47	0.50	V
		I <sub>F</sub> = 1.0 A, T <sub>J</sub> = 125 °C		0.36	0.42	
Maximum Reverse Leakage Current	I <sub>rm</sub>	V <sub>r</sub> = 30 V		0.004	0.100	mA
		V <sub>r</sub> = 30 V, T <sub>J</sub> = 100 °C		0.7	10	
		V <sub>r</sub> = -30 V, T <sub>J</sub> = 125 °C		3.0	20	
Junction Capacitance	C <sub>T</sub>	V <sub>r</sub> = 10 V		50		pF

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

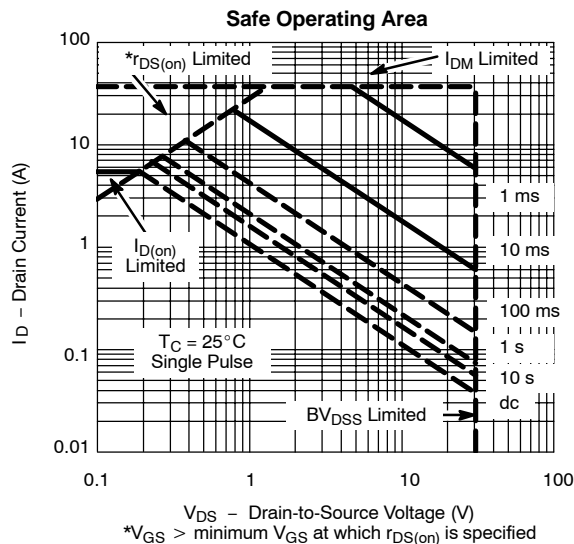
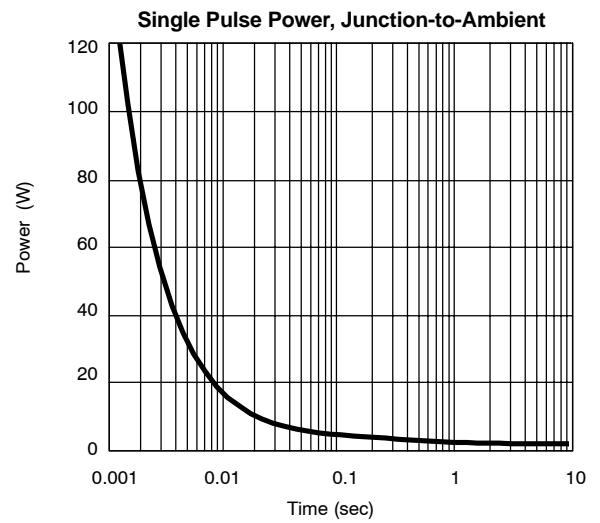
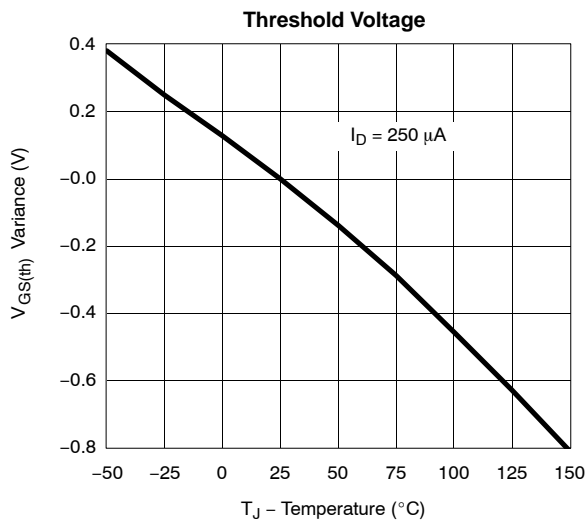
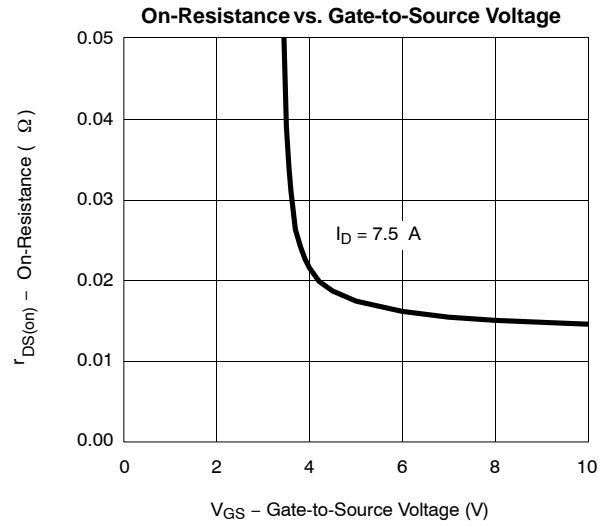
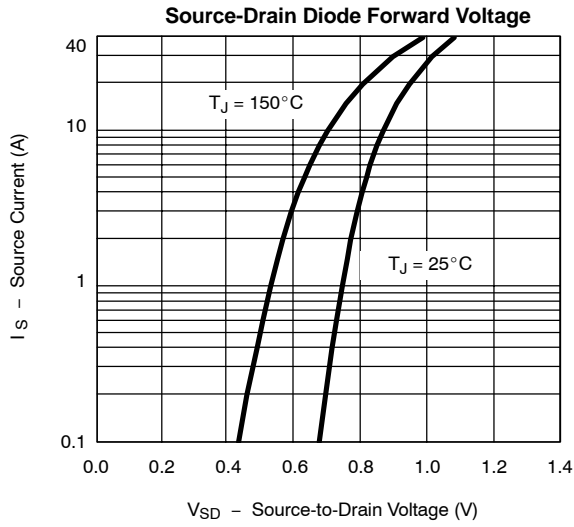
**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**

**CHANNEL-1**



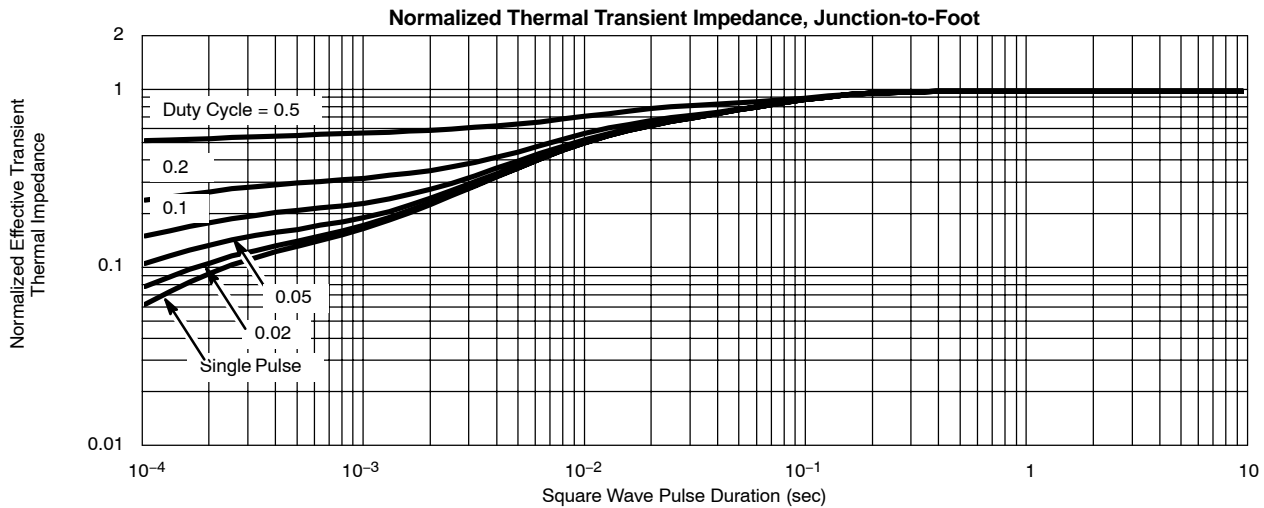
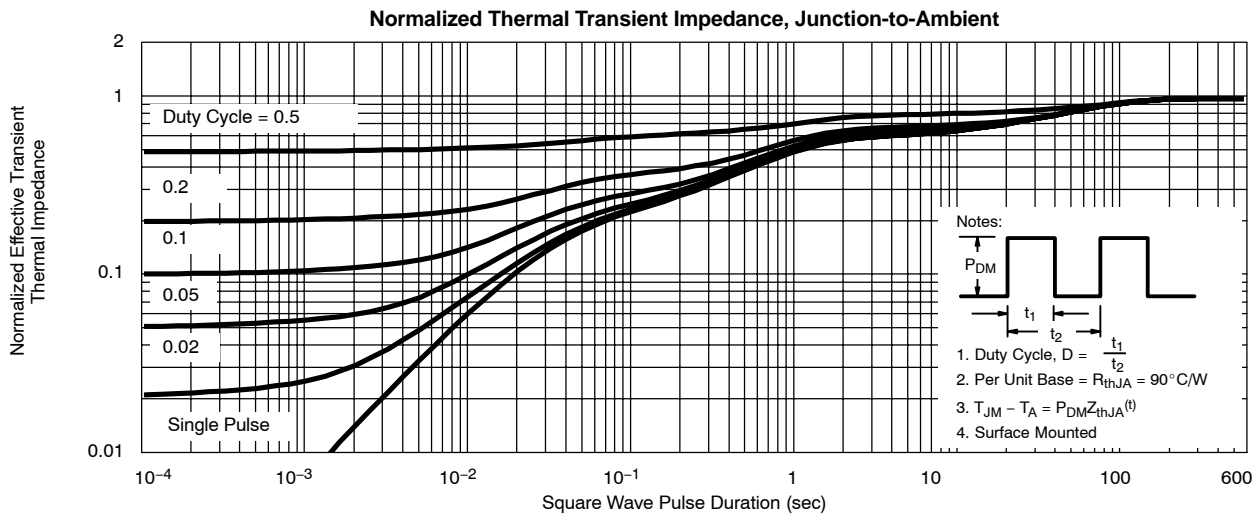


**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED) CHANNEL-1**



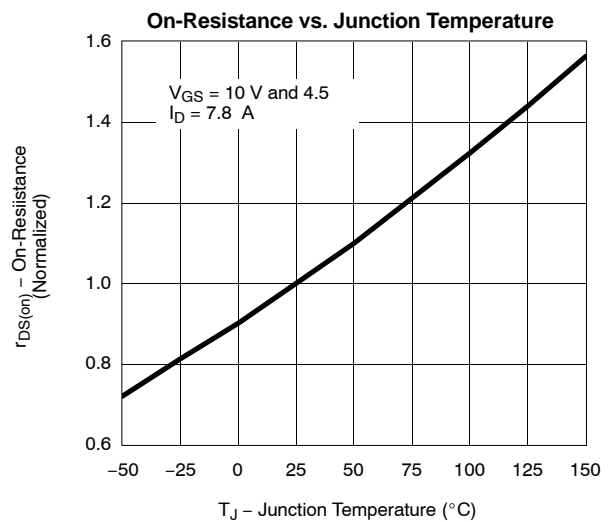
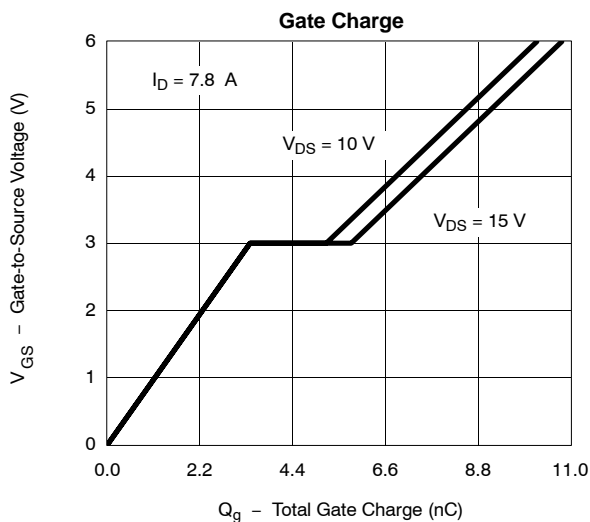
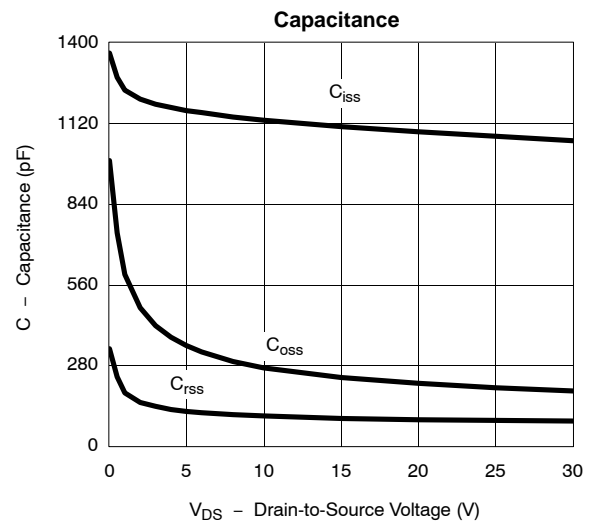
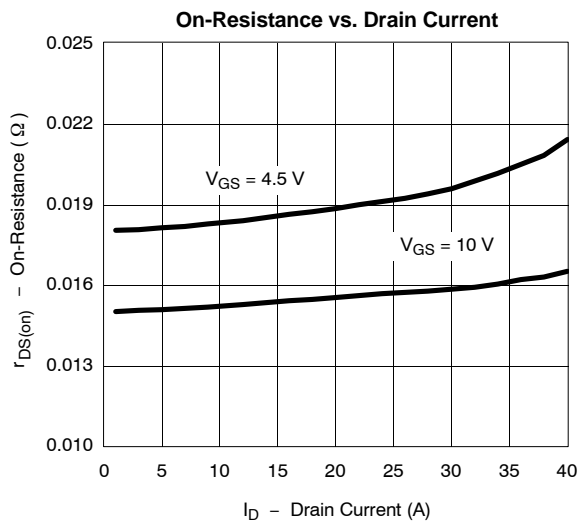
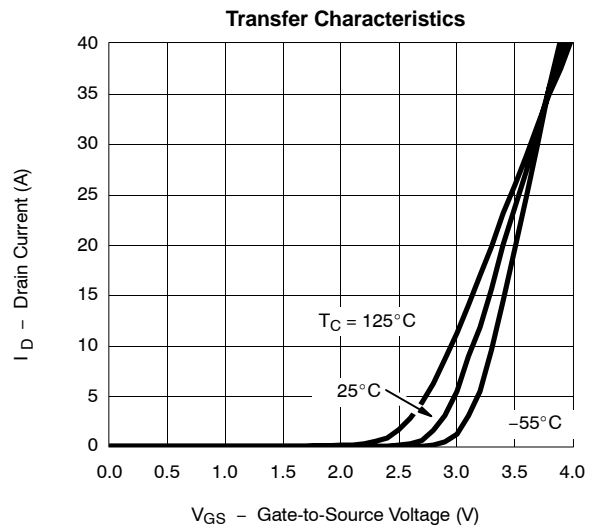
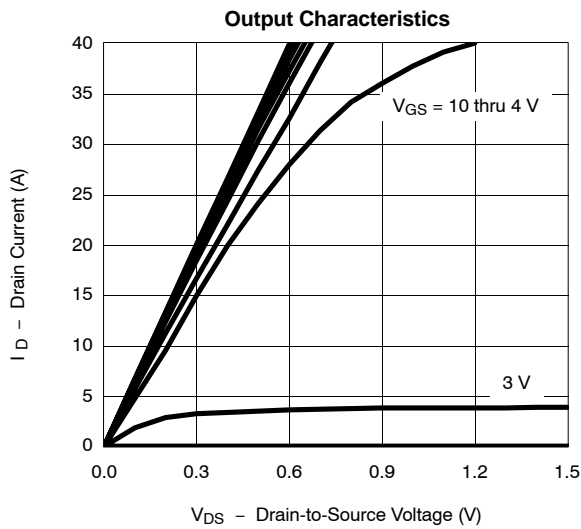
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**CHANNEL-1**



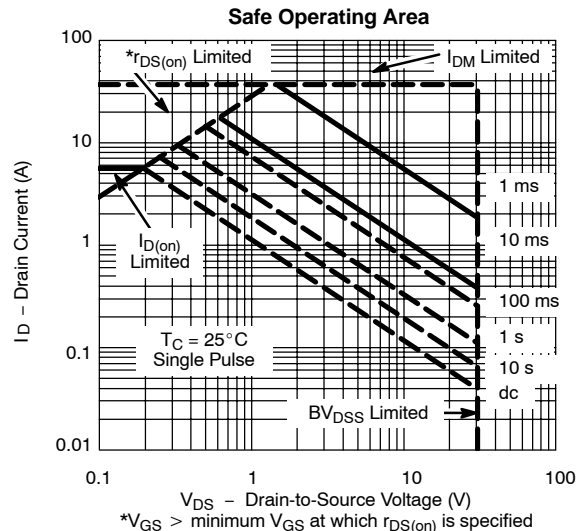
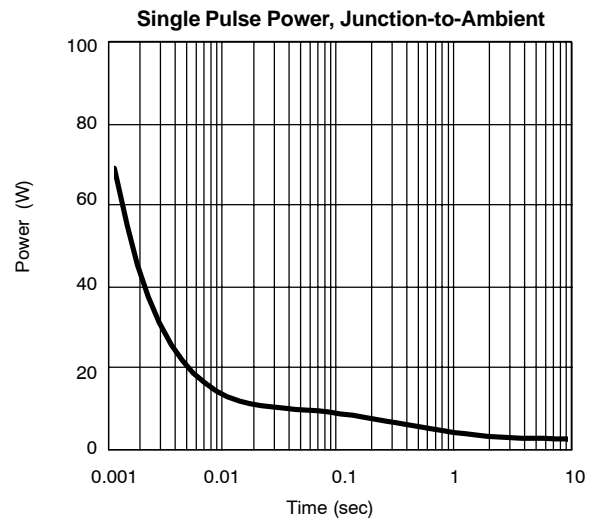
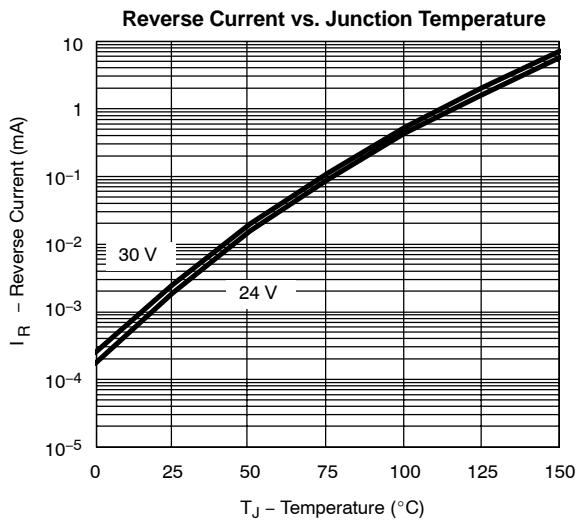
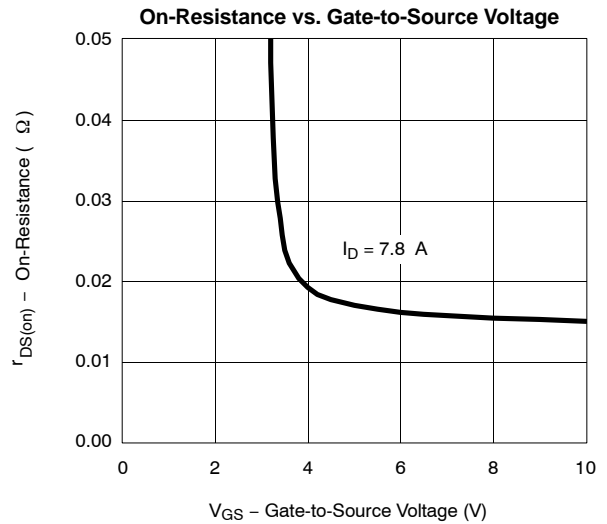
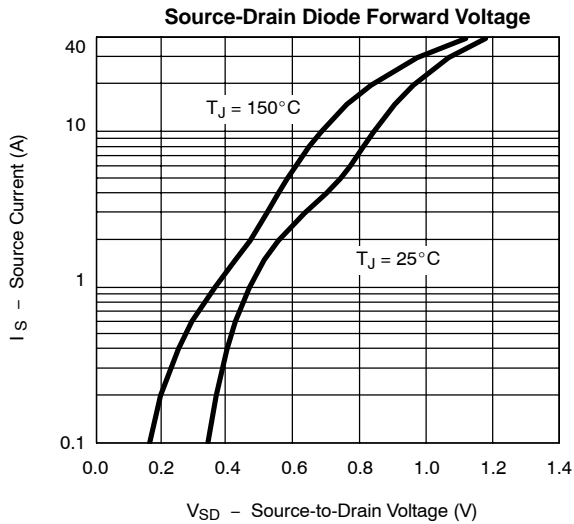


**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED) CHANNEL-2**



**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

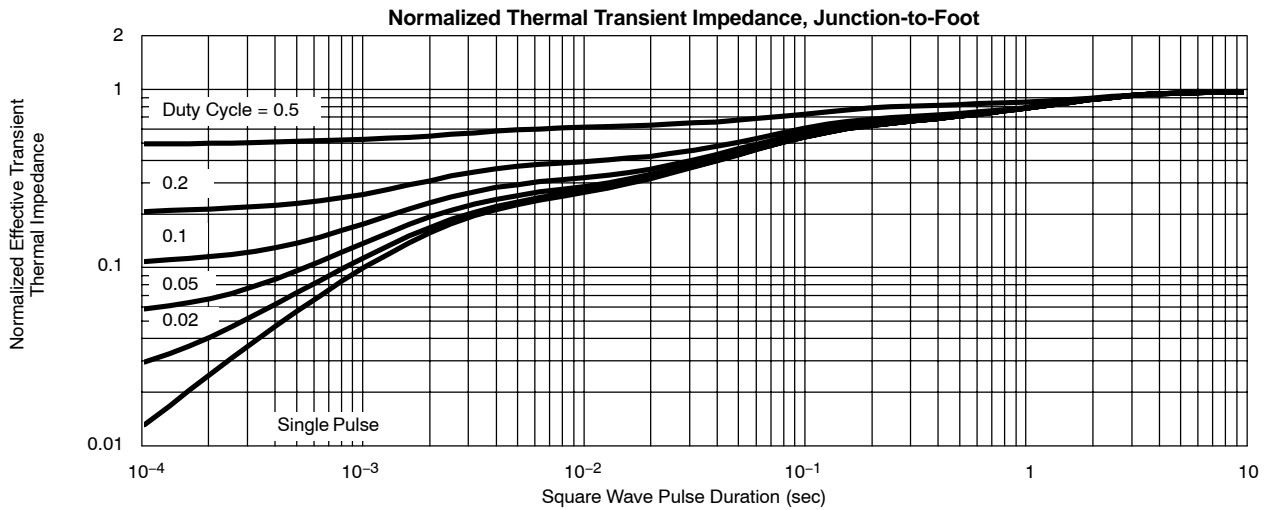
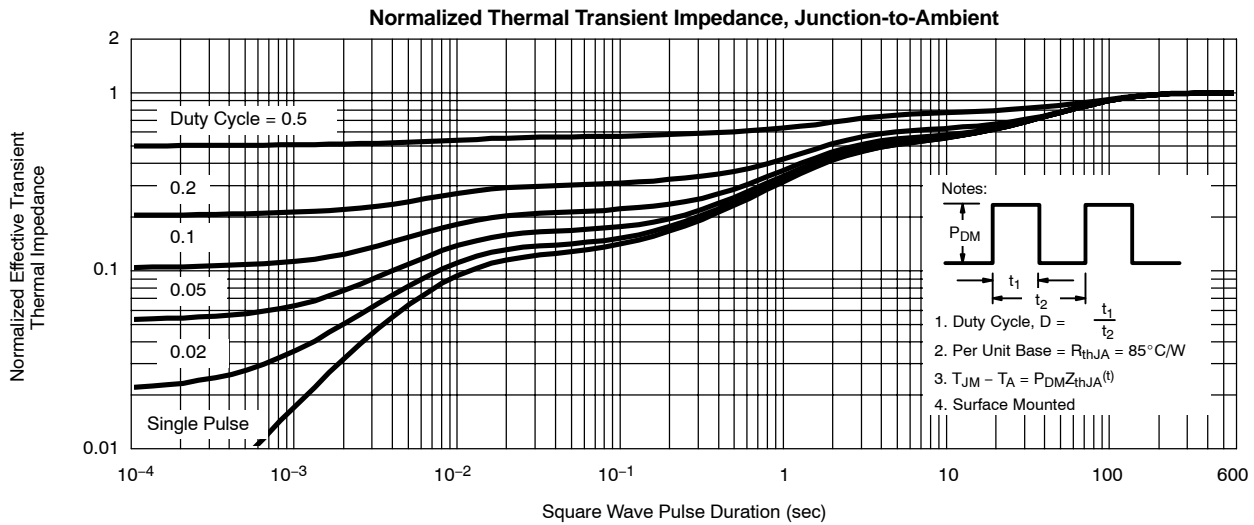
**CHANNEL-2**







**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED) CHANNEL-2**



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?73278>.